



General approach to automation of FLASH subsystems



Agenda



- Motivation
- Nature of the problem and underlying information
- Required capabilities
- Basic formalization of problem domain
- Anatomy of the solution



Motivation



Necessity of uniform approach to design and implementation of automation software for high energy experiments.

Ultimate role of the automation software:

- Maximization of lasers availability.
 - Automation of **routine activities** as startup, shutdown ...
 - Continuous **monitoring** of hardwares condition.
 - Human error minimalization.
 - **Autorecovery** from specific trips.
- Improvement of **breakdown cause location**.

Important software engineering challenges:

- Standardization of **design, implementation** and **documentation** of automation software for laser subsystem.
- Development of **formal verification** and **testing** procedures, thereby improvement of **reliability, predictability** and **safety** of the software.



From the outset: underlying information



- Online **measurements** and **status signals** form the hardware and their meaning
- Definitions of **operation modes** of the subsystem
- Definitions of known **fault patterns** and remedial measures
- Primitive **procedures** and **actions** that are primary tools of the automation



Basic formalization

- **Observables:**
Input DOOCS signals of types int,bool,symbolic.
- **Quantized condition:**
Symbolic variable with limited domain. Its value is based on values of the observables
- **State of the subsystem:**
Valuation of all quantized conditions
- **Operation mode:**
Valuation of set of quantized conditions
- **Procedure:**
Procedure + valuations that its execution entails
- **Long run sequence:**
Sequence of procedures bringing the subsystem from current to the desired operation mode.
- **Fault pattern:**
Valuation of set of observables
- **Exception:**
Classified fault pattern with ascribed remedial procedure.



Automation capabilities



Required capabilities:

- To automate **achieve** and **maintain** pre-selected operation mode
- To adapt to unpredictably changing conditions of the hardware
- To recover the subsystem from known faults.
- To persist in above processes to **reasonable extent**

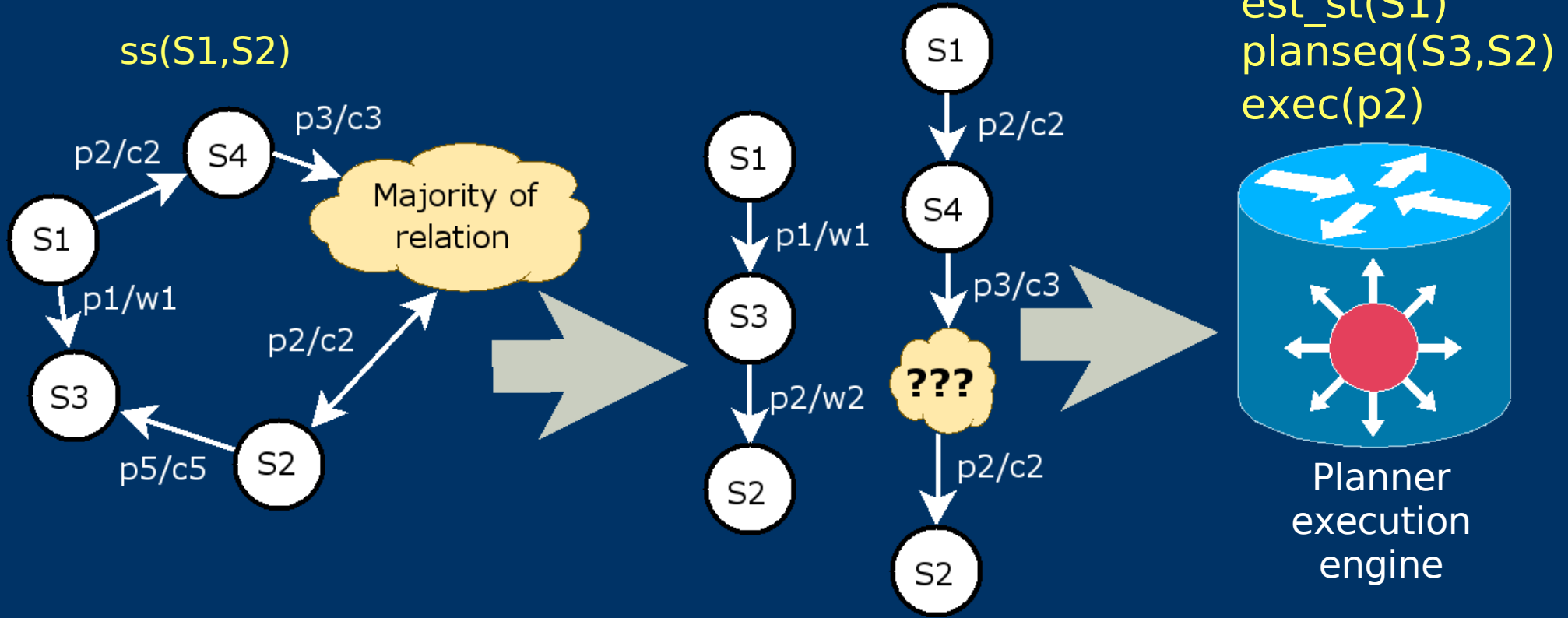


Anatomy of the solution - planner

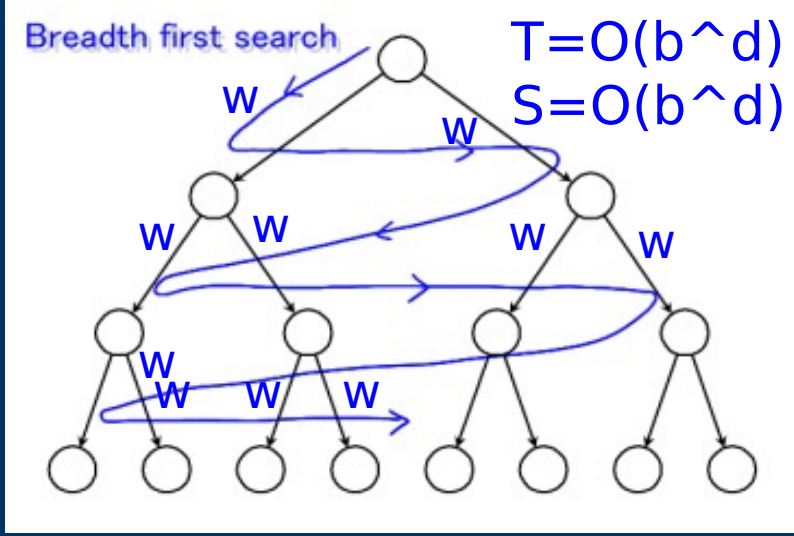
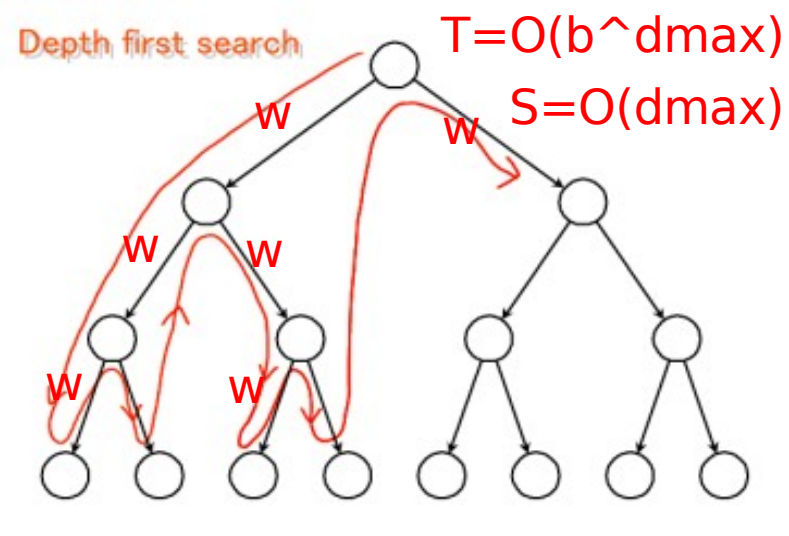
Ensemble of:

- State estimator
- Transition relation (state space)
- Planning (path finding) algorithm
- Execution engine

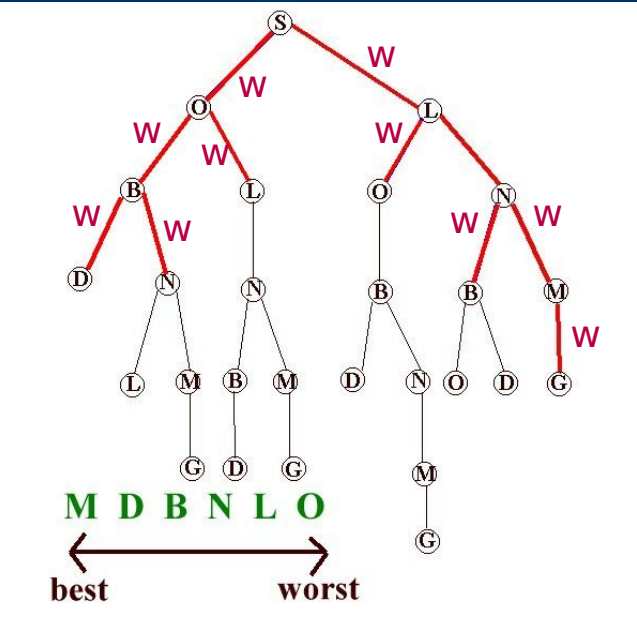
$$\text{planseq}(S1,S2) = \{p1,p2,\dots,pn\}$$



$est_st(S1)$
 $planseq(S1,S2)$
 $exec(p1)$
 $est_st(S1)$
 $planseq(S3,S2)$
 $exec(p2)$



Best first search



$f(n) = w_i + h(n)$
 $h(n) = w_1 + w_2 + \dots$

$T = O(b^d)$
 $S = O(b^d)$

$T = O(d^b)$
 $S = O(d^b)$

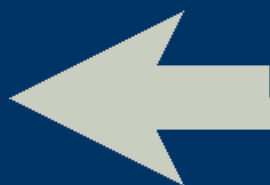
Now used:

- Depth first search with iterative deepening
 $T = O(d), S = O(b^d)$
- Cycles prevention
- Hill climbing

Role of exception handler execution engine:

- Discovering the fault patterns
- Conflict resolution based on faults categorization
- Execution of remedy procedure for encountered faults

Exception handler's engine



← TTF2.RF/KLY.INTERLOCK/KLY5/KLYS1
← TTF2.RF/KLY.INTERLOCK/KLY5/KLYS2
← TTF2.RF/KLY.INTERLOCK/KLY5/KLYS3
← TTF2.RF/KLY.INTERLOCK/KLY5/KLYS4

⋮

← TTF2.RF/KLY.INTERLOCK/KLY5/KLYS12
← TTF2.RF/KLY.INTERLOCK/KLY5/KLYS13
← TTF2.RF/KLY.INTERLOCK/KLY5/KLYS14
← TTF2.RF/KLY.INTERLOCK/KLY5/KLYS15

Definitions of exceptions
{Sig pattern, class,
procedure, message}



Pattern matching + conflict resolution



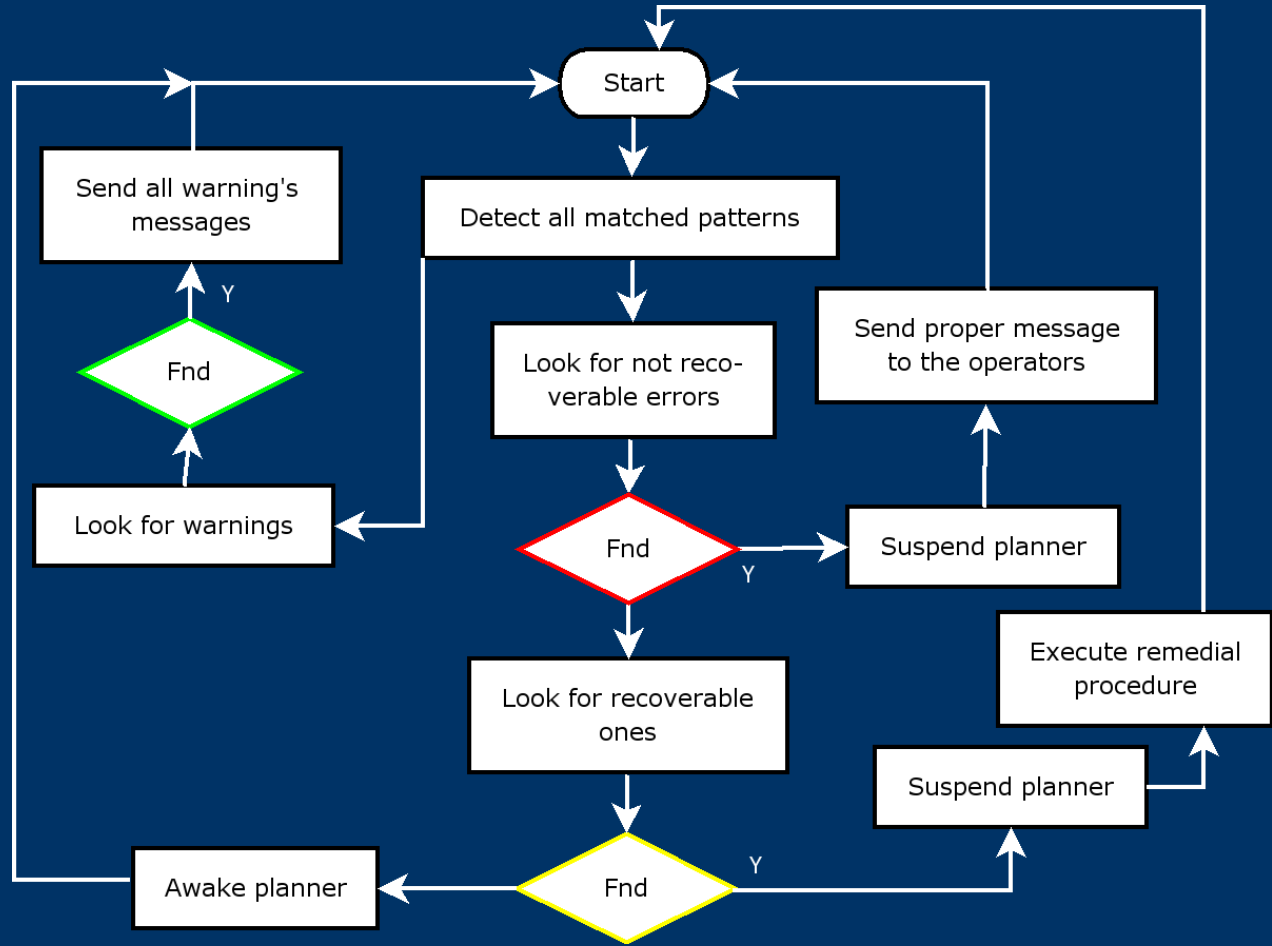
Conflict resolution:

- Fault arbitration based on
 - × Categorization
 - × Order of definition in specification file

Classes of faults:

- Warnings
- Recoverable
- Not recoverable

Exception handler's policy:



Scenario 1

- SE cannot estimate state
- indicate “incomplete”
- ask if EH can solve problem
- **not**
- remain in “incomplete”
- SE estimated state
- go back to automatic operation

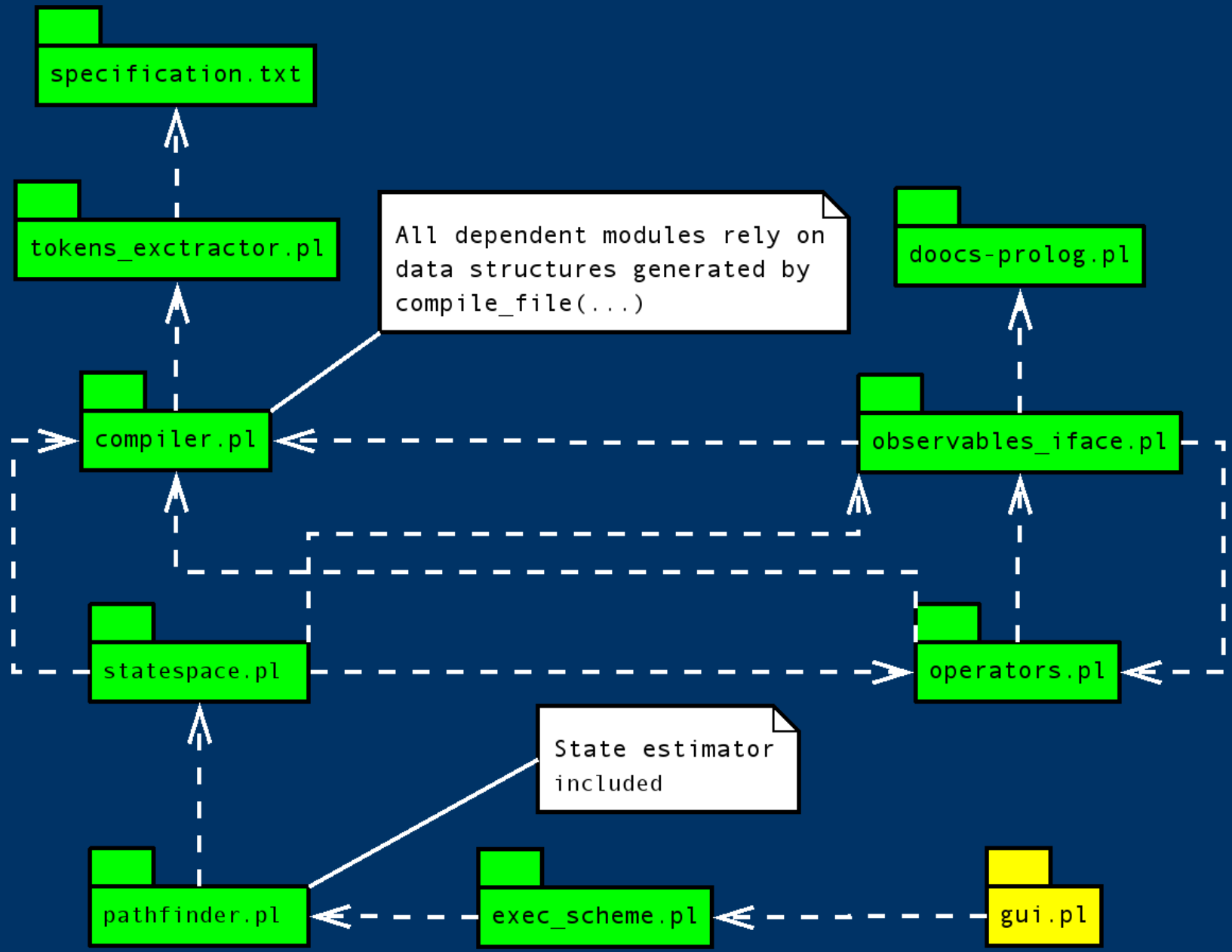
Scenario 2

- exceptions detected
- **disable planner**
- planner is suspended
- **choose exception with the highest priority**
- **execute remedial procedure**
- **no exceptions**
- **awake planner**
- planner back in operation



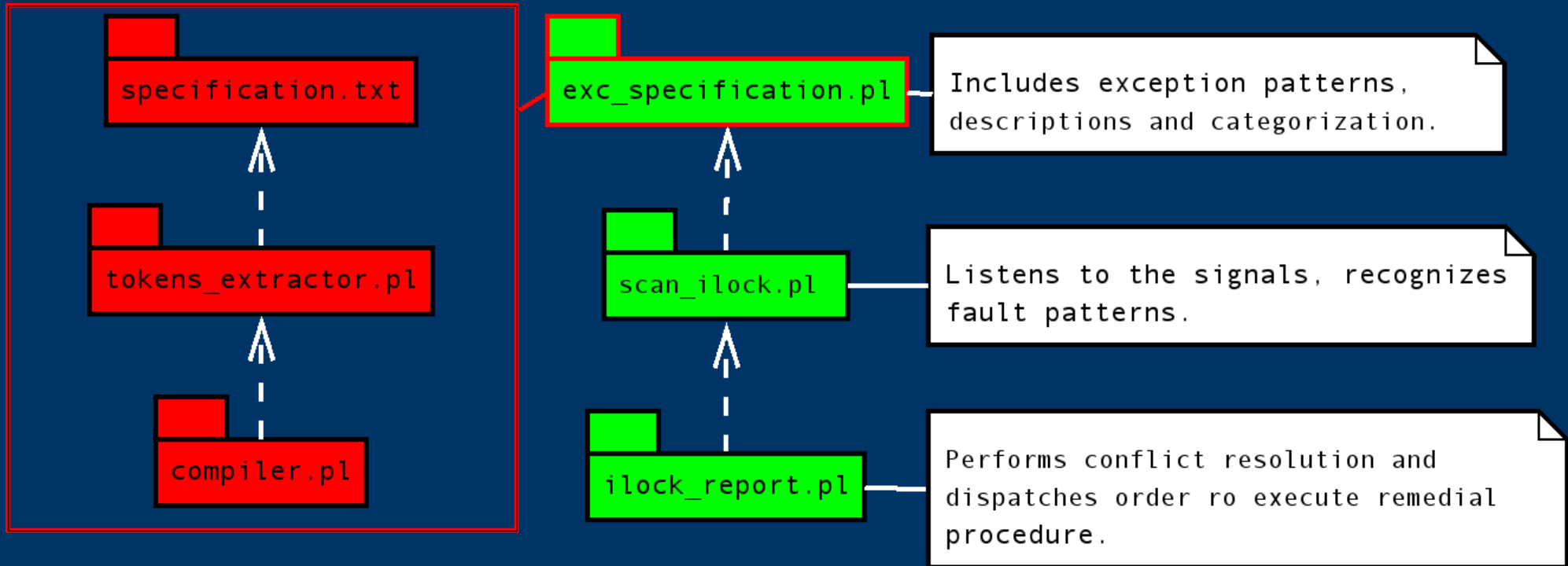


Implementation - planner



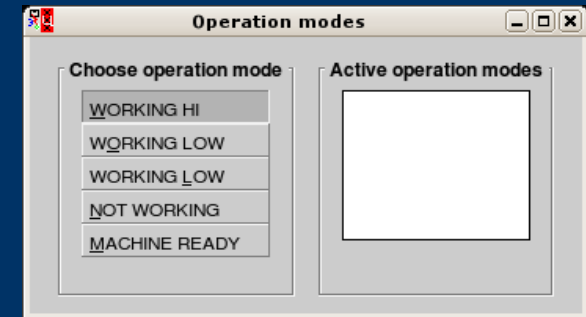
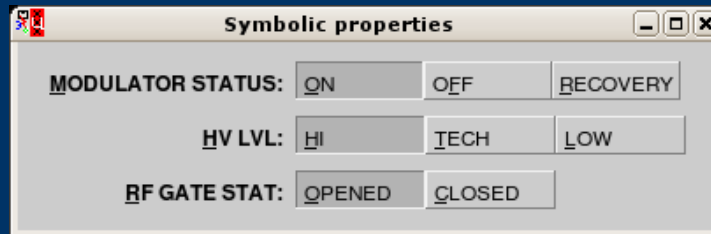
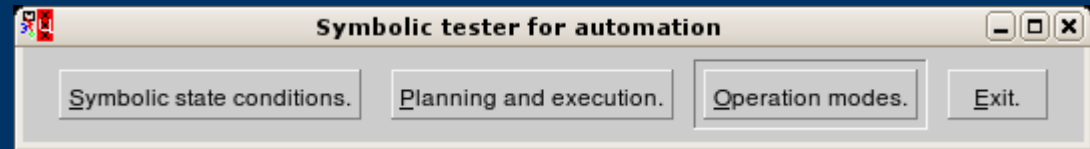
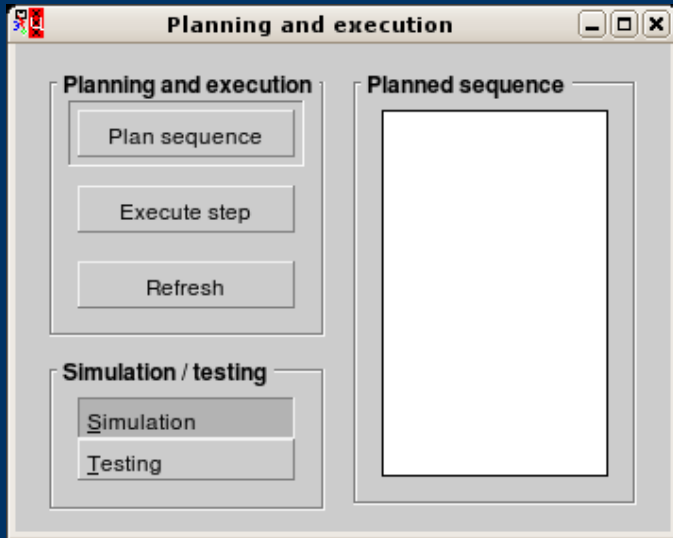


Implementation – exception handler

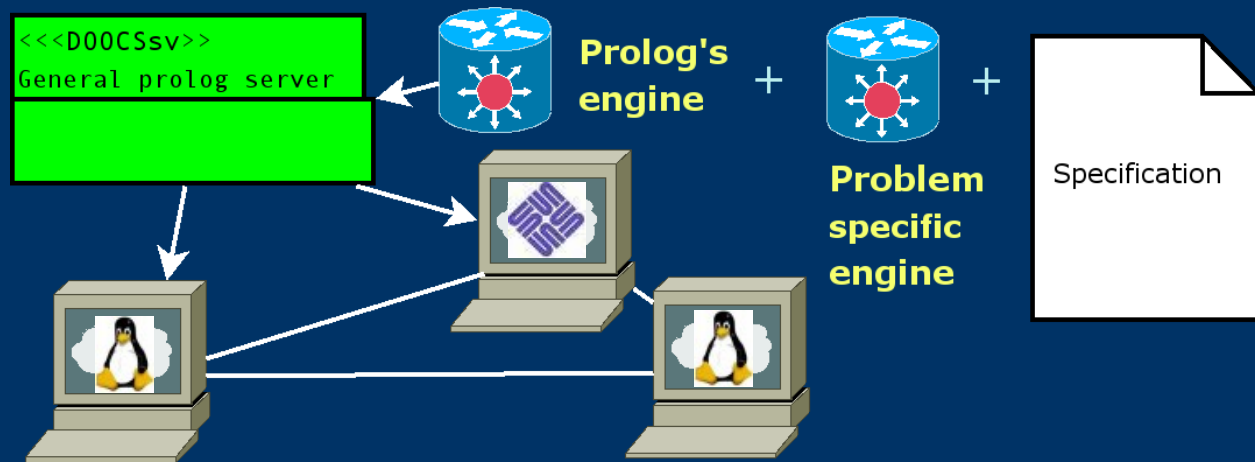




Twofold deployment



```
bogus@mskbkoseda: ~/grammar
Plik  Edycja  Widok  Terminal  Zakładki  Pomoc
bogus@mskbkoseda: ~/grammar  x  doocsadm@mskbkoseda: ~/doocs/L...  x
% operators.pl compiled 0.00 sec, 3,400 bytes
% statespace.pl compiled 0.00 sec, 4,340 bytes
% pathfinder.pl compiled 0.00 sec, 3,892 bytes
% exec_scheme.pl compiled 0.00 sec, 6,700 bytes
Warning: (/home/bogus/grammar/gui.pl:129):
Singleton variables: [DlgRef, MenuRef]
Warning: (/home/bogus/grammar/gui.pl:130):
Singleton variables: [Cond]
Warning: (/home/bogus/grammar/gui.pl:134):
Redefined static procedure pretty_print/1
% gui.pl compiled 0.00 sec, 11,568 bytes
% loader.pl compiled 0.04 sec, 208,844 bytes
Yes
?- estimate_state(X).
X = [eq('MODULATOR STATUS', 'ON'), eq('HV LVL', 'TECH'), eq('RF GATE STAT', 'CLOSED')] []
```





To be continued ...



Thank You



Bogusław Kosęda